	Туре	L#	Hits	Search T xt	DBs	Time Stamp
1	BRS	L1	241	diaghragm	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:42
2	BRS	L2	104	1 and (housing)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:42
3	BRS	L3	87	2 and (chamber\$ compartment\$ area)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:43
4	В̀RS	L4	81	3 and (first second other another)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:44
5	BRS	L5	1	4 and (exhaust near4 path)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:52

	Туре	L#	Hits	Search Text	DBs	Tim Stamp
6	BRS	L6	0		USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:54
7	BRS	L7	12	4 and (control near4 unit ECU controller microprocessor)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:55
8	BRS	L8	6	7 and (exhaust)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:56

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	Туре	L#	Hits	S arch Text	DBs	Tim Stamp
1	BRS	L1	241	diaghragm	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:42
2	BRS	L2	104	1 and (housing)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:42
3	BRS	L3	87	2 and (chamber\$ compartment\$ area)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:43
4	BRS	L4	81	3 and (first second other another)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:44
5	BRS	L5	1	4 and (exhaust near4 path)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:52

	Туре	L#	Hits	Search T xt	DBs	Tim Stamp
6	BRS	L6	0		USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:54
7	BRS	L7	12	4 and (control near4 unit ECU controller microprocessor)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:55
8	BRS	L8	6	7 and (exhaust)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 11:56
9	BRS	L9	126252	((communication) near10 (chamber compartment area))	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 12:01
10	BRS	L10	4	8 and ((communication) near10 (chamber compartment area))	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_TD B	2002/11/27 12:01

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